



Continuation of Substance of Interview including description of the general nature of what was discussed: Applicant's amendment dated August 27, 2009 overcame the prior art of record however it was the position of the Examiner that amendments to claim 9 did not appear to overcome newly cited prior art of Hieda. In particular, Hieda (Fig. 5) shows parallel connected FET switches each comprising an inductor and connected to each other thru intermediate switch 2a. Fig. 11 shows a SPDT switch. It was the position of the Examiner that dependent claim 10 would allowable comprising similar limitations of the parallel inductor connected to the series circuit of FET and capacitor also included in claim 4. It was agreed to combine claims 9 and 10 by Examiner's amendment to place the claims in condition for allowance.